



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/632,273
Filing Date July 31, 2003
Inventor..... Warren M. Farnworth et al.
Assignee Micron Technology, Inc.
Group Art Unit..... 2829
Examiner Russell M. Kober
Attorney's Docket No. MI22-2379
Title: Method and Apparatus for Testing Semiconductor Circuitry for Operability and
Method of Forming Apparatus for Testing Semiconductor Circuitry for Operability

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patent listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

Citation of this reference is respectfully requested.

Respectfully submitted,

Date: 10-6-05

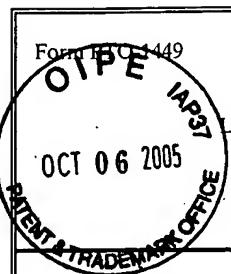

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 LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2379	SERIAL NO. 10/632,273			
				APPLICANT Warren M. Farnworth	FILING DATE July 31, 2003	GROUP 2829		
U.S. PATENT DOCUMENTS								
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,172,050	12/92	Marvin Swapp	324	999.999		
	AB	6,686,758	02/04	Farnworth, et al.	324	765		
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
	AL							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AM							
	AN							
	AO							
	AP							
	AQ							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR							
	AS							
EXAMINER				DATE CONSIDERED				
*EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								